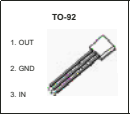




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A97<5B=75@'85H5

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Parameter	mbo	a e	Jnit
Input Voltage	V _i	35	V
Thermal Resistance from Junction to Ambient	R _{θJA}	166.7	°C/W
Operating Junction Temperature Range	T _{OPR}	-25~+125	°C
Storage Temperature Range	T _{STG}	-65~+150	°C

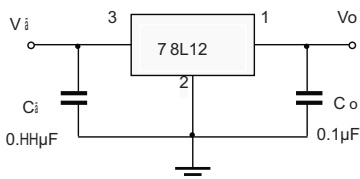
9@97HF:75@'7<5F57H9F:GH:7G'5H'GD97:::98`J:FHI5@'>|B7H:CB`H9AD9F5HI F9'

ÇX&MFJXÉQ [M I € { ÇÉÖÅM€ÉHH`ØÉÉÖ [M€ÉF`ØÉÁ` } | ^••A [c@^! , à•^•] ^&â:âÅADÁ

Parameter	mbo	e t on ition	V _{in}	V _a	Jnit		
Output voltage	V _o	25°C	11.5	12	12.5	V	
		0-125°C	14V≤V _i ≤27V, I _o =1mA-40mA	11.4	12	12.6	V
			I _o =1mA-70mA	11.4	12	12.6	V
Load Regulation	ΔV _o	I _o =1mA-100mA	25°C	22	100	mV	
		I _o =1mA-40mA	25°C	13	50	mV	
Line regulation	ΔV _o	14.5V≤V _i ≤27V	25°C	55	250	mV	
		16V≤V _i ≤27V	25°C	49	200	mV	
Quiescent Current	I _q	25°C	4.3	6.5	mA		
Quiescent Current Change	ΔI _q	16V≤V _i ≤27V	0-125°C	1.5	mA		
	ΔI _q	1mA≤I _o ≤40mA	0-125°C	0.1	mA		
Output Noise Voltage	V _N	10Hz≤f≤100KHz	25°C	70	μV/V _o		
Ripple Rejection	RR	15V≤V _i ≤25V, f=120Hz	0-125°C	37	42	dB	
Dropout Voltage	V _d	25°C	1.7	V			

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Note : Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the pins of the regulator.

